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CONFIRMATION NO. 5592

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APPLICANTS Ken Idota, Osaka, JAPAN; Teruhito Ohnishi, Osaka, JAPAN; Akira Asai, Uehonmachi-Osaka, JAPAN;				
** CONTINUING DATA ***** This application is a CON of PCT/JP02/09569 09/18/2002				
** FOREIGN APPLICATIONS ***** JAPAN 2001-283272 09/18/2001				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/05/2004				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged		STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 11
Examiner's Signature _____ Initials _____		INDEPENDENT CLAIMS 2		
ADDRESS 20277				
TITLE METHOD OF FABRICATING A BIPOLAR TRANSISTOR UTILIZING A DRY ETCHING AND A WET ETCHING TO DEFINE A BASE JUNCTION OPENING				
FILING FEE RECEIVED 1070	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	